

The Future of Automotive Compute

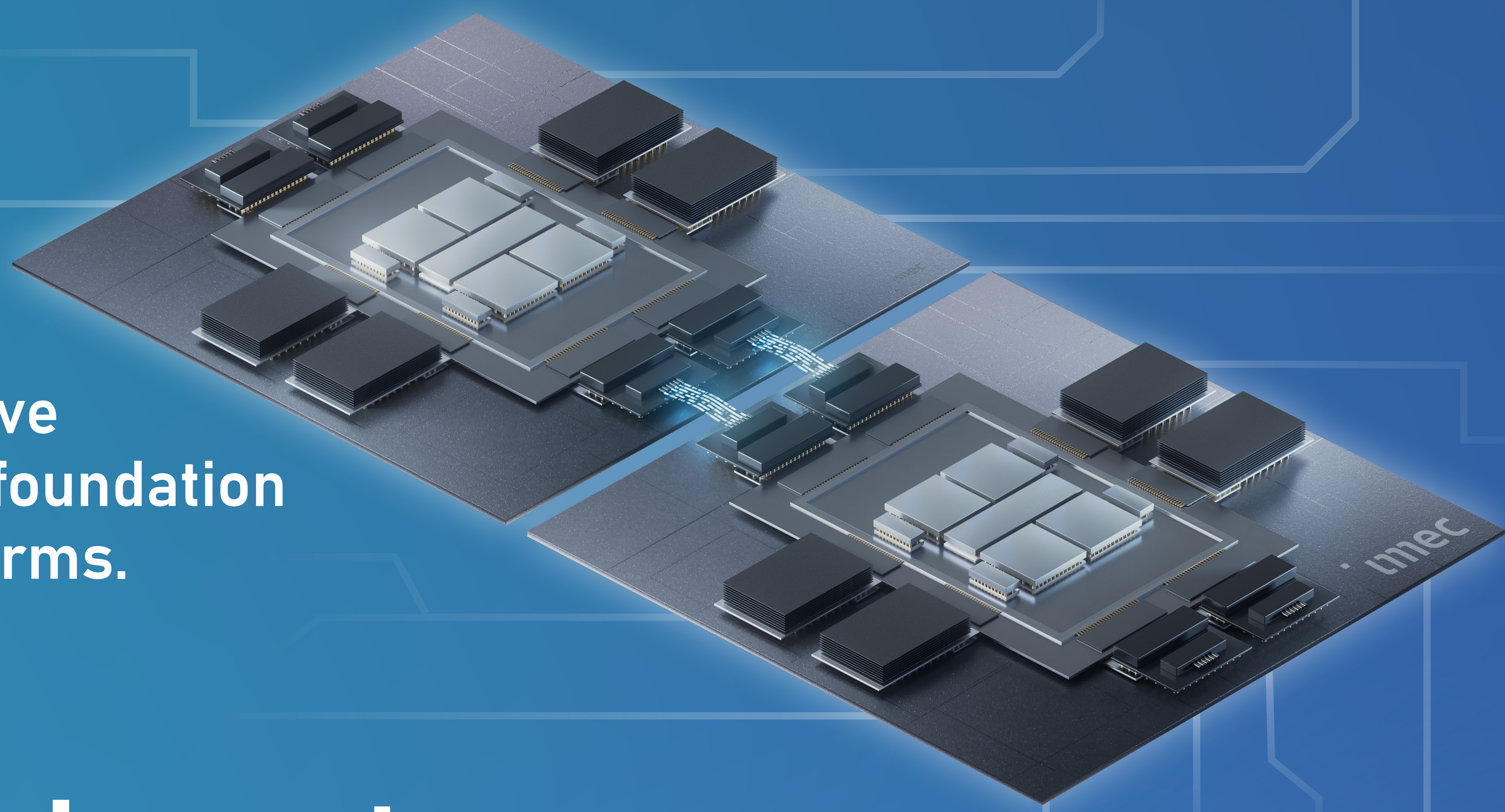
From Chiplet Architectures to Software-Defined Vehicles

Vision

CHASSIS enables European semiconductor sovereignty through an open automotive chiplet ecosystem, proofing flexible, automotive-grade compute platforms for the next generation of Software Defined Vehicles.

Mission

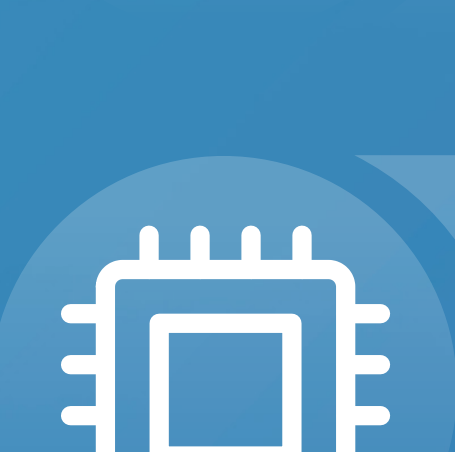
CHASSIS unites Europe’s semiconductor and automotive leaders in building the open foundation for the future compute platforms.



Impact

CHASSIS strengthens Europe’s competitiveness by fostering semiconductor innovation, creating resilient value chains, and enabling tailored SoC platforms that meet the evolving needs of automakers.

Outcomes

- 
 Automotive Base Die
- 
 Virtual/Hybrid Dev-Platforms
- 
 Multi-Vendor Integration Framework
- 
 SoC Prototype & Software Stack
- 
 In-Vehicle Demonstrator



www.automotive-chiplets.org



Co-Funded by the European Union



Funded by the Chips JU Grant No 101252788